

FIG. 1a

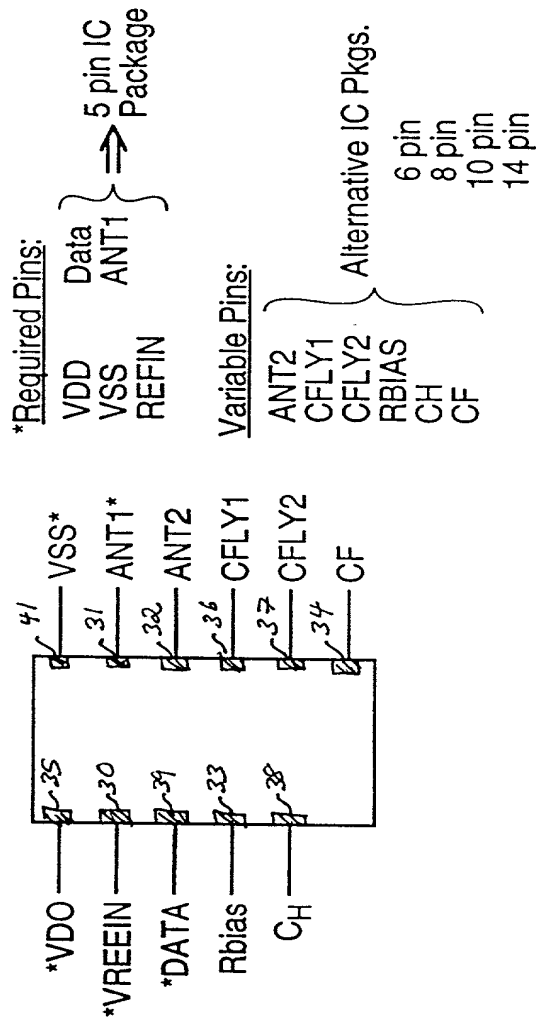
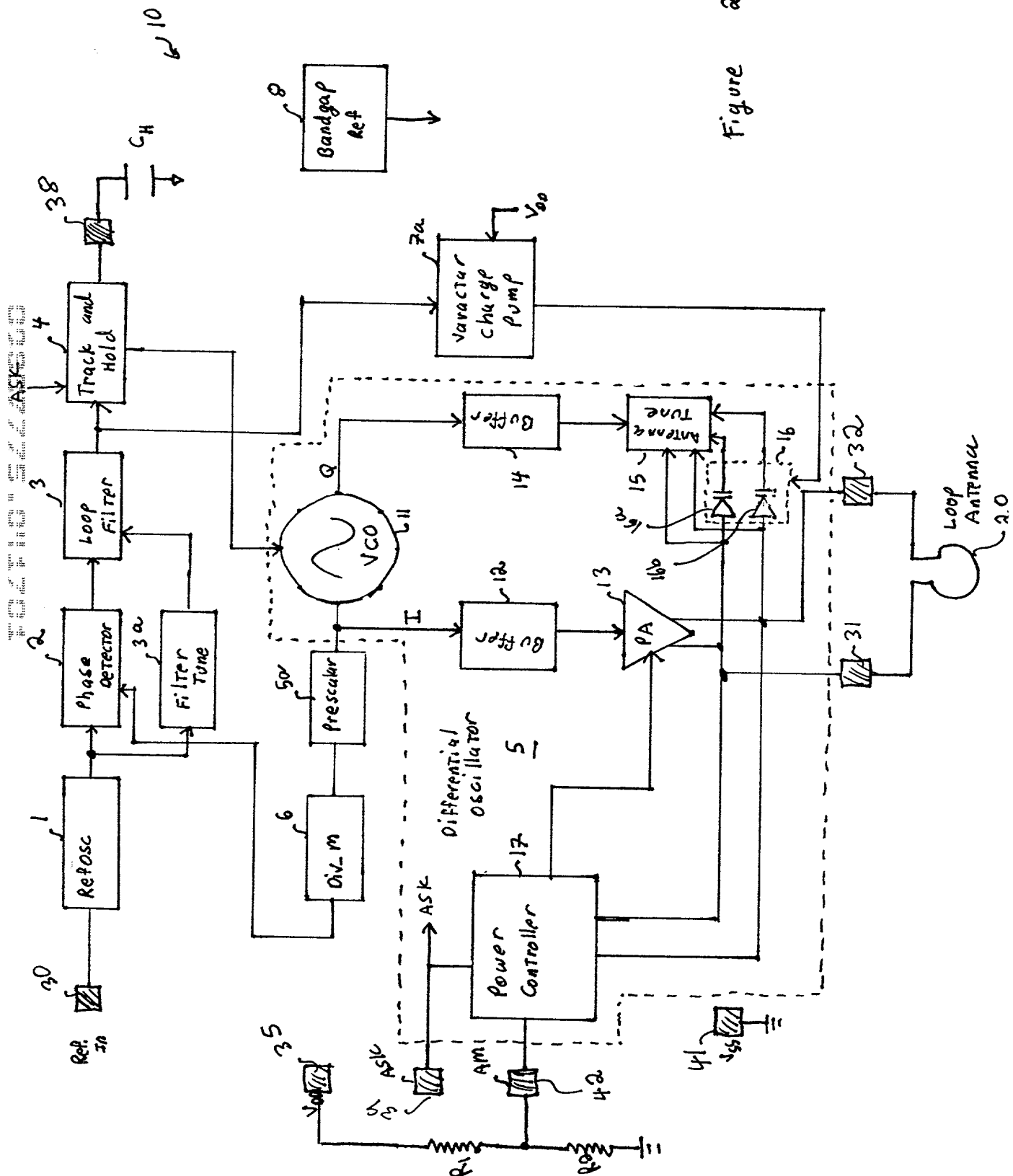


FIG. 1 b



CONNECTION PIN DIAGRAM FOR THE INVENTION  
FULLY INTEGRATED, ALL CMOS ASK/AM TRANSMITTER WITH AUTOMATIC ANTENNA TUNING

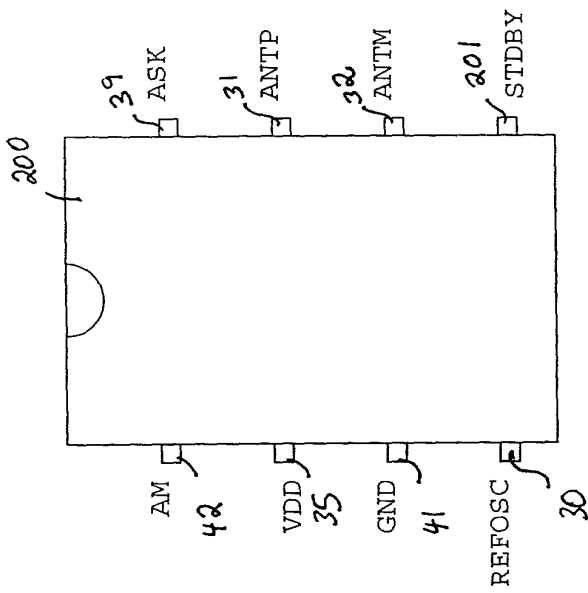


FIGURE 2b

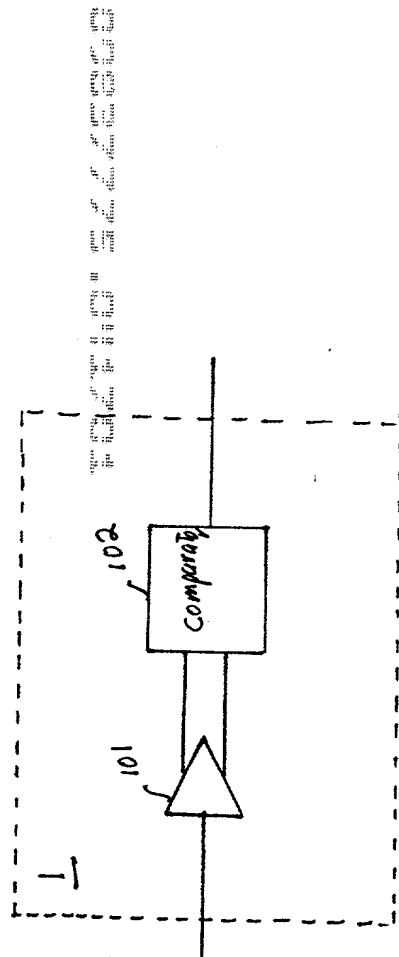


Figure 3a

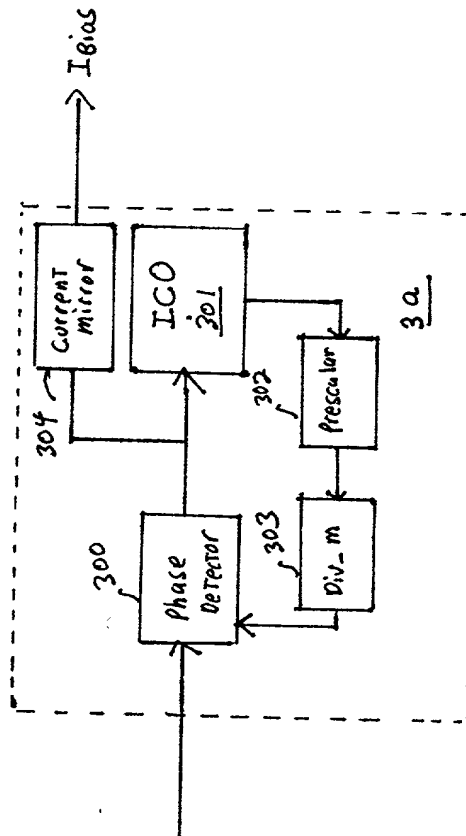


Figure 3b

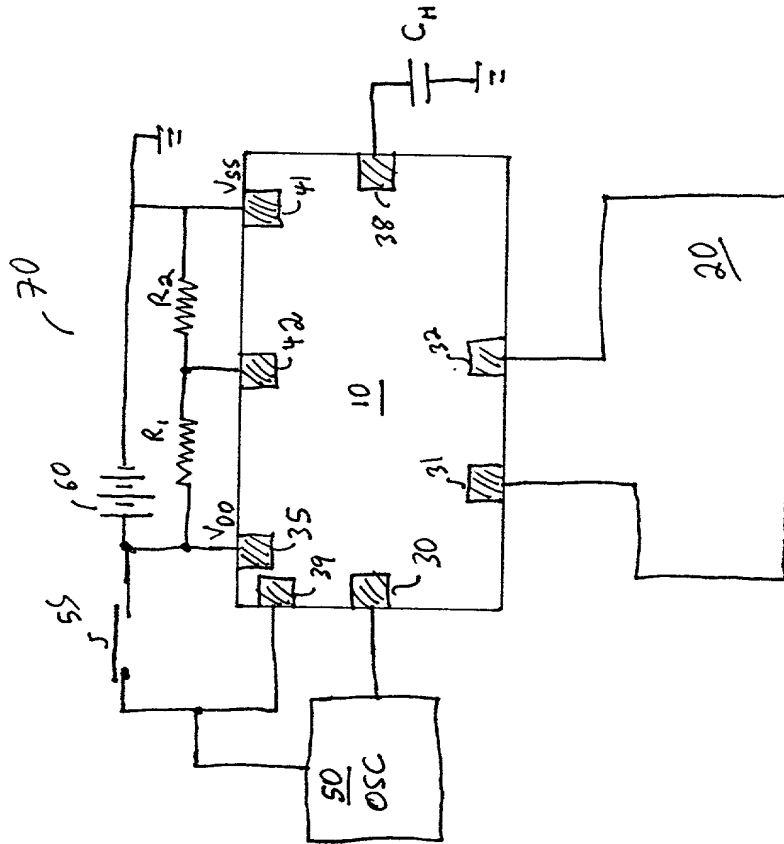


Figure 4

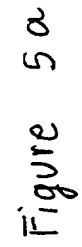


Figure 5a

CLOSED LOOP POWER CONTROL

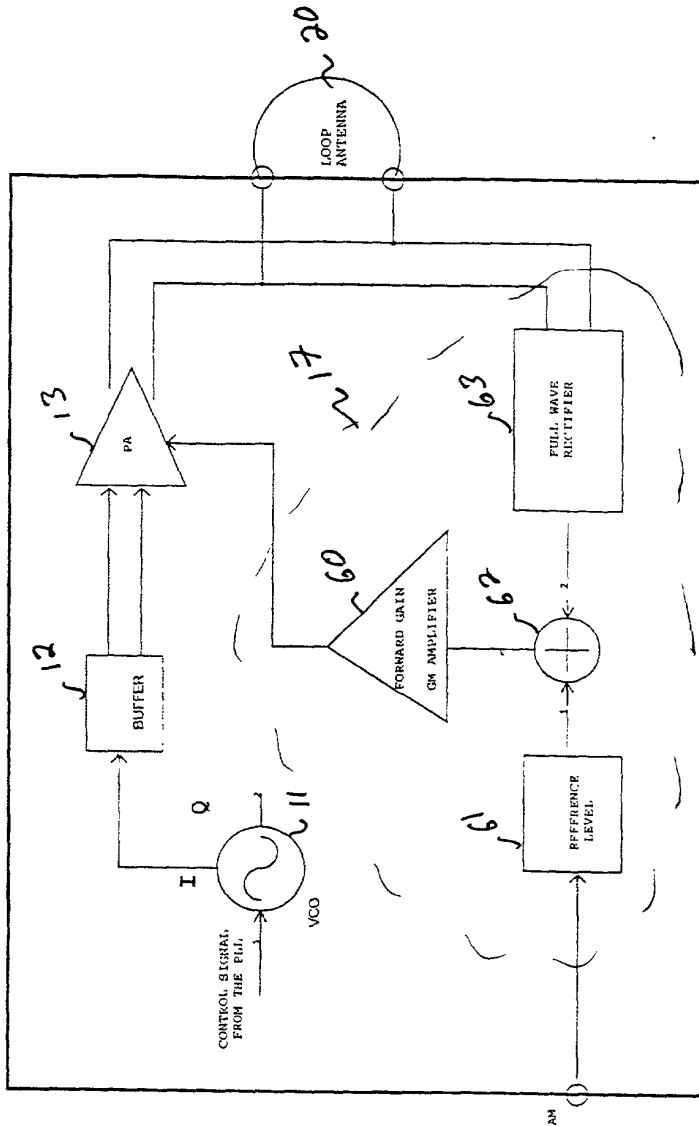
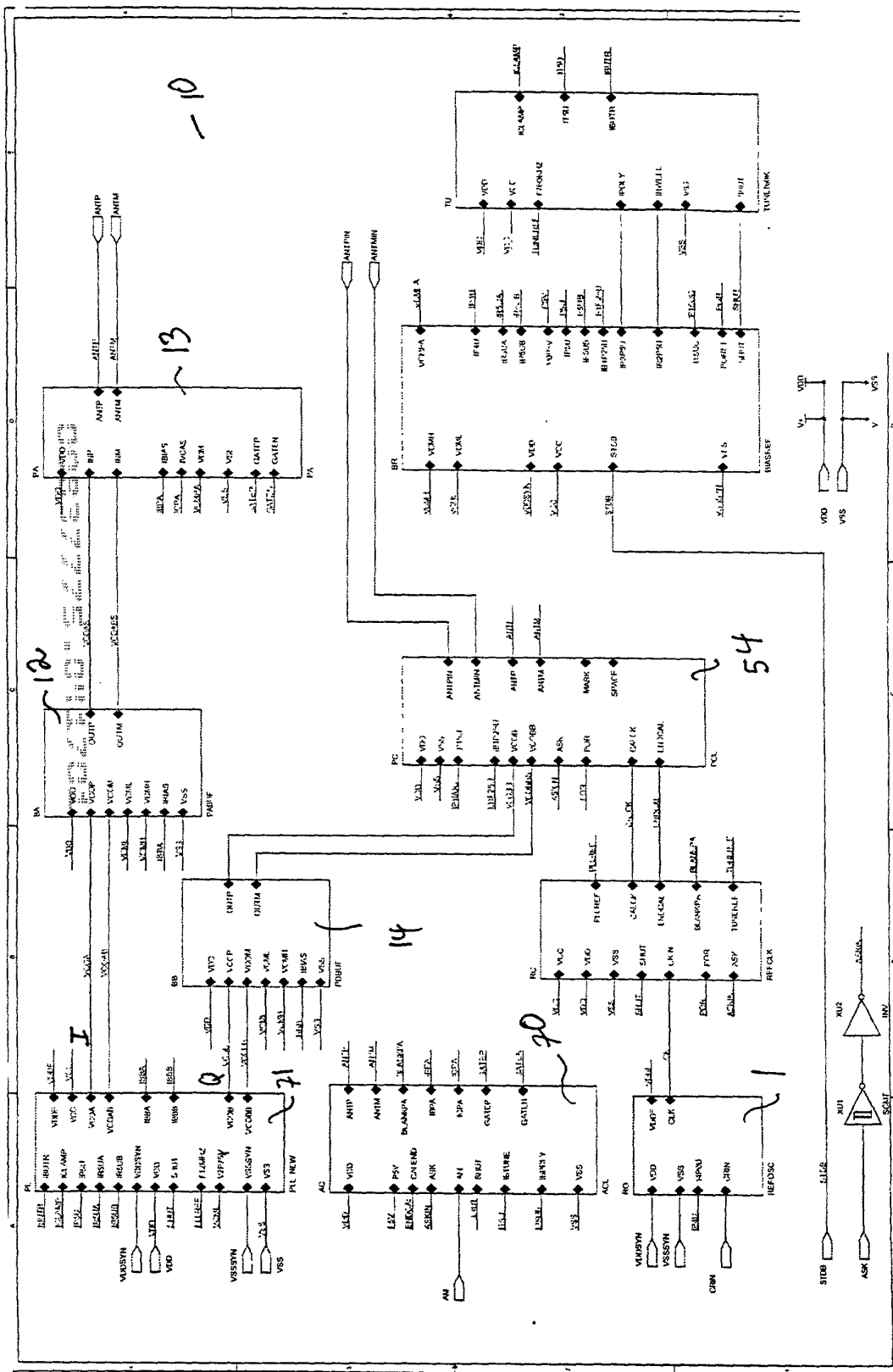


Figure 5b





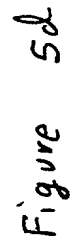


Figure Sd

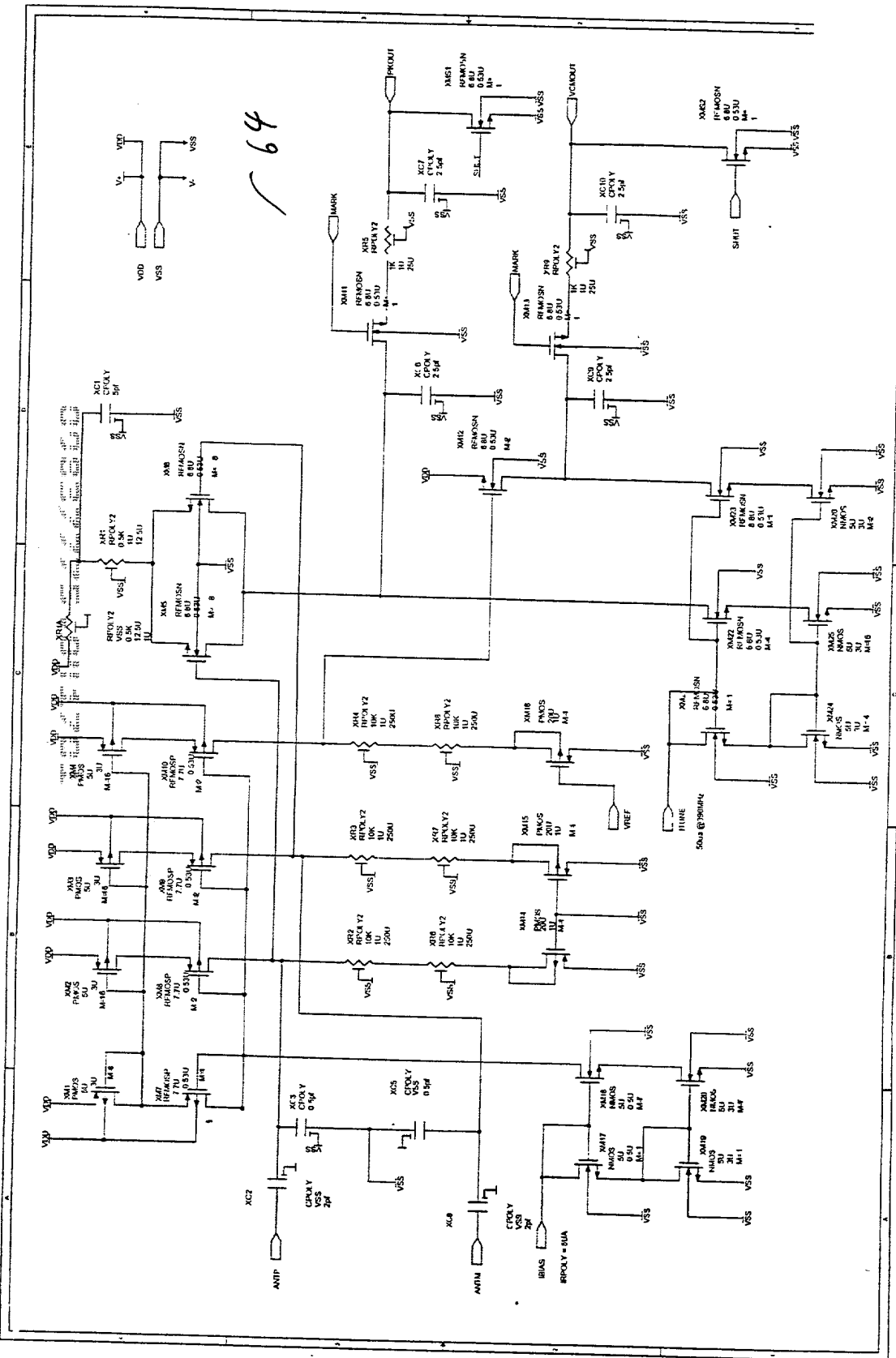


Figure 5c

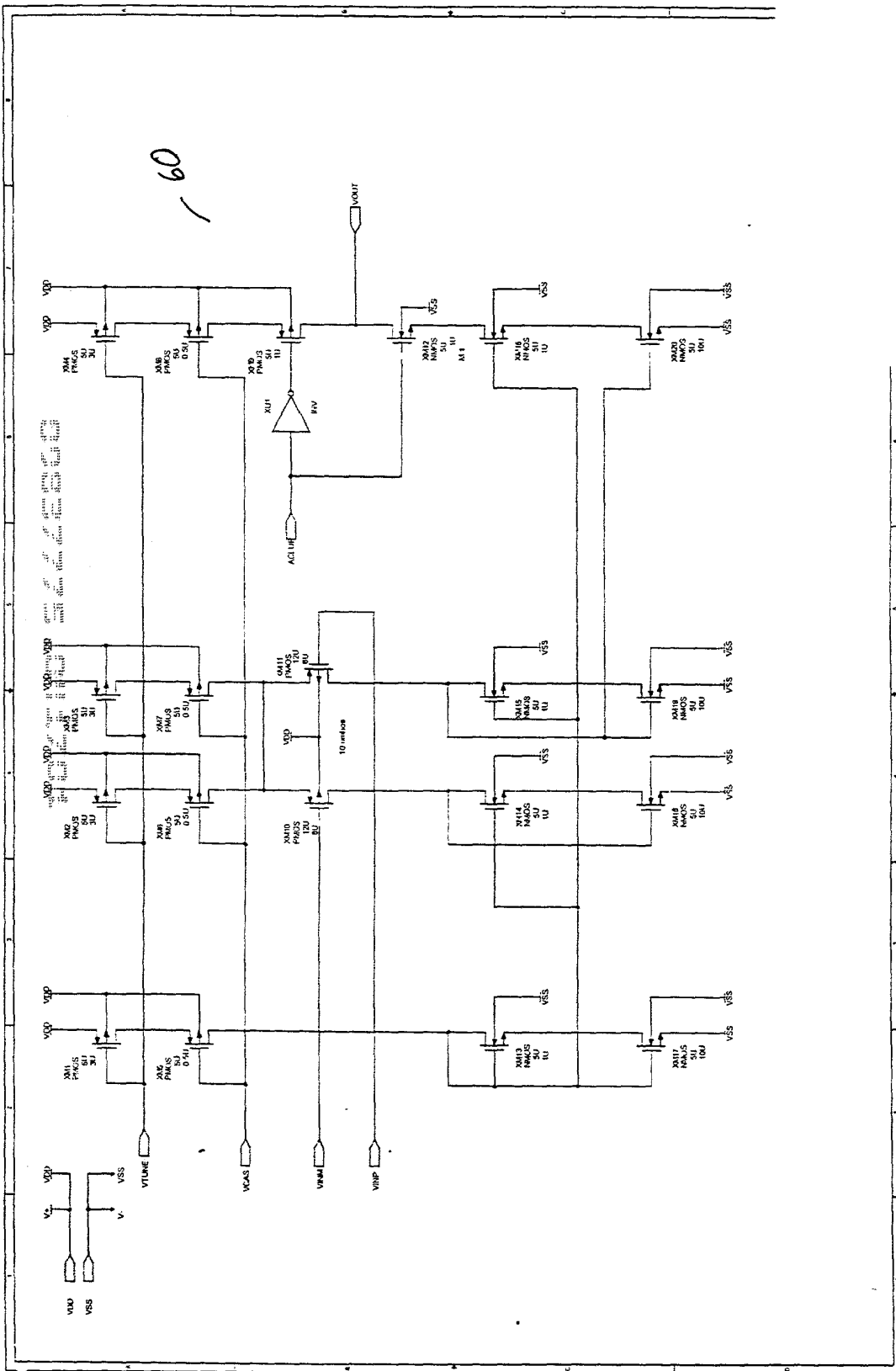


Figure 5A

POWER AMPLIFIER

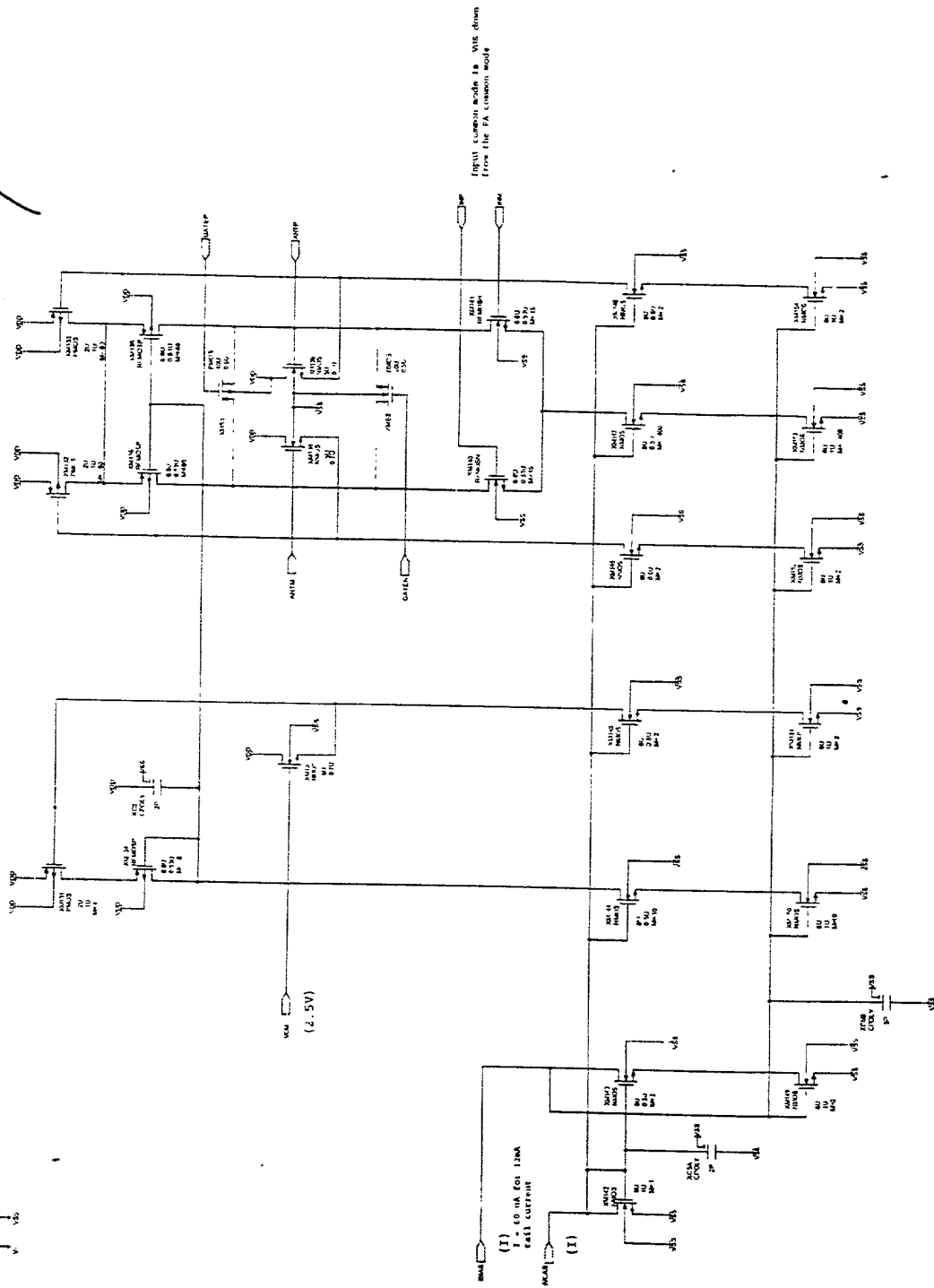
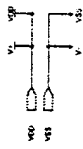


Figure 5g

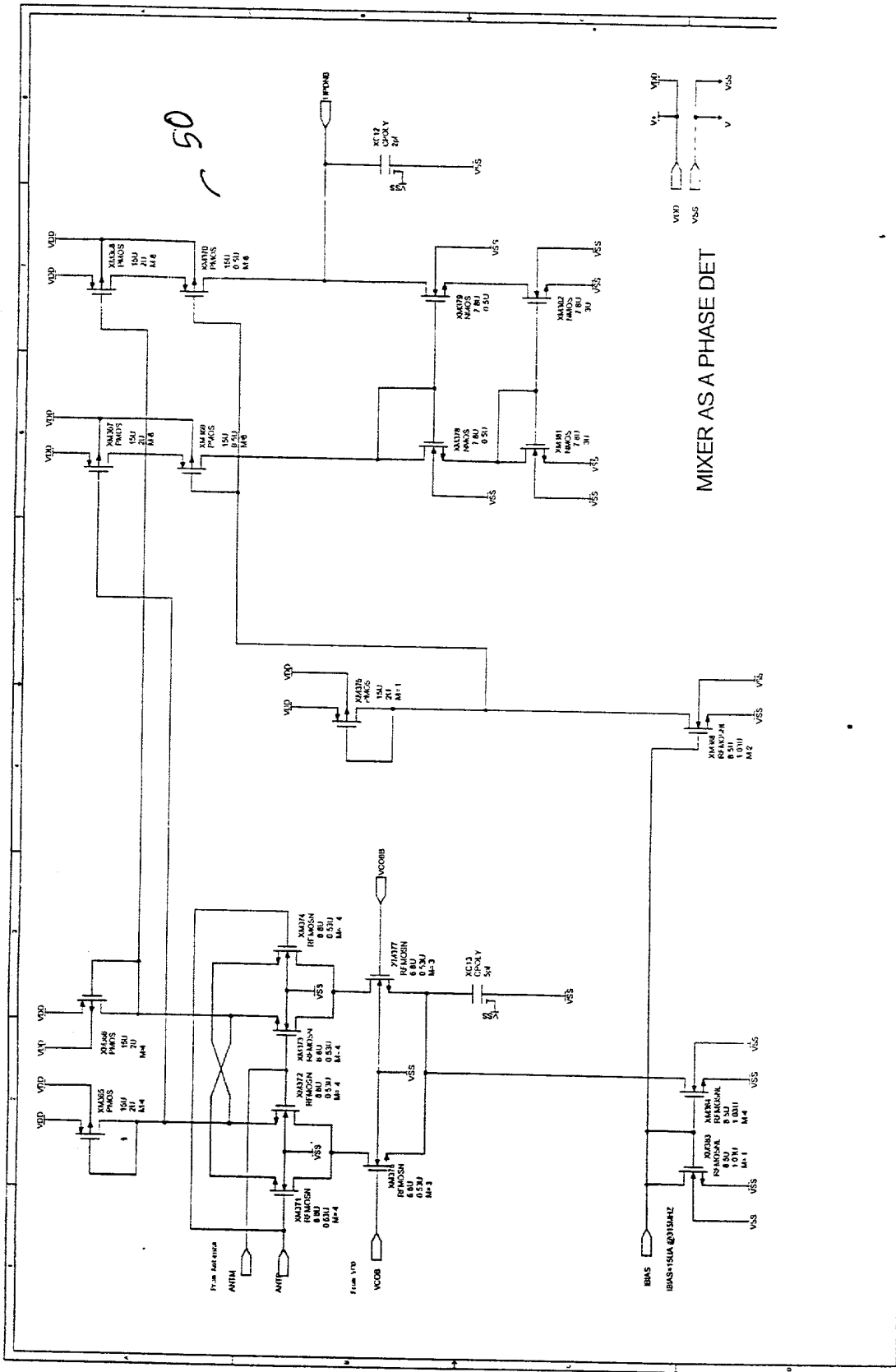


Figure 50

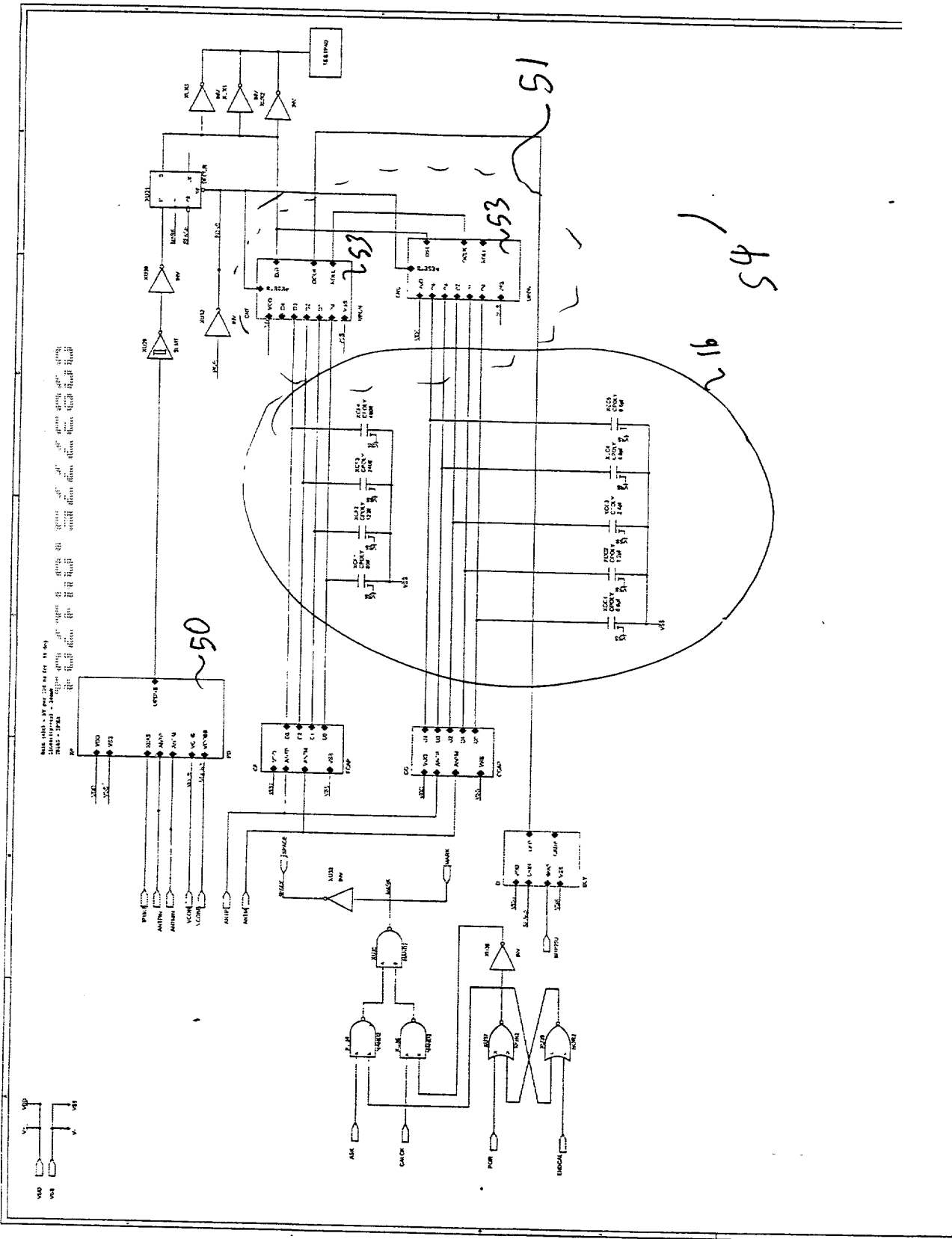
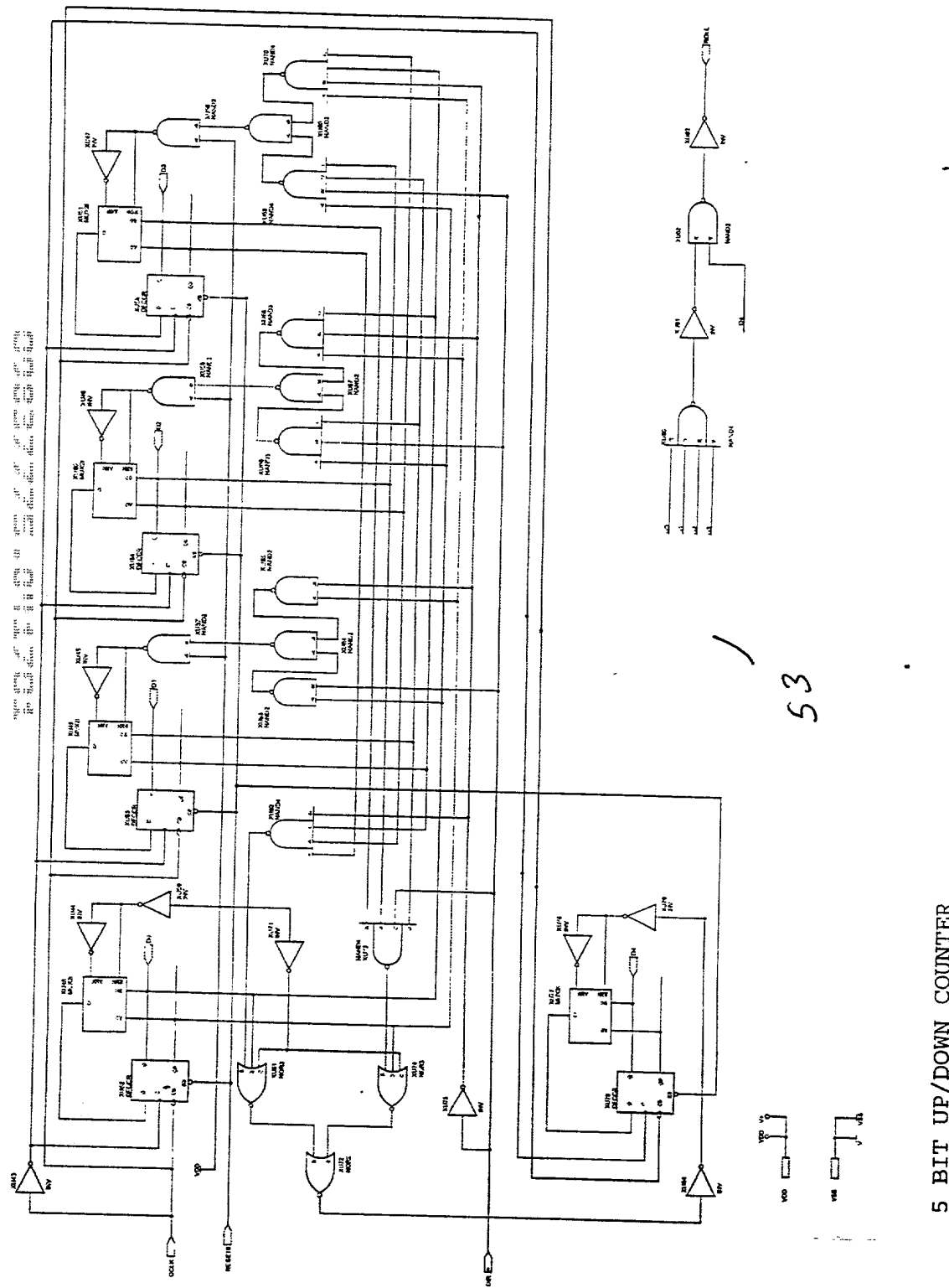


Figure 5A



5 BIT UP/DOWN COUNTER

Figure 5j